Gp2681 #5 EN 6/18/03

8028-1027

PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Eiji KITO

Confirmation No. 5099

Serial No. 09/911,537

Group 2681

Filed jULY 25, 2001

COMMUNICATION SYSTEM FOR TRANSFERRINGLARGE DATA FROM NETWORK INTERFACE TO RADIO INTERFACE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents

Washington, D.C. 20231

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying documents, copies of which are attached to this statement, are made of record on the enclosed sheet.

A concise explanation of the relevance of these items is that these references were cited by the Japanese Patent Office in the corresponding Japanese Application Serial No. 2000-224108, filed July 25, 2000. A copy of the Japanese Official Action in which they were cited is attached hereto, with what is believed to be the pertinent portion enclosed in a wavy line. An English translation of the enclosed portion is also attached hereto.

Under the provisions of 37 CFR 1.97(e), the undersigned hereby certifies that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign Patent Office in a

counterpart foreign application not more than three months prior to the filing of this Statement.

Respectfully submitted,

YOUNG & THOMPSON

By _____

Robert J. Patch
Attorney for Applicant
Registration No. 17,355
745 South 23rd Street
Arlington, VA 22202
Telephone: 703/521-2297

May 5, 2003

KITO - U.S. Pat. Appl. 09/911,537
Ref. F-11530

Record (See the Reference Citation List to obtain the citation)

Claims 1-4
Citations 1-2

Remarks:

In Fig. 25 of Citation 1 reference is made to a variable rate transmission device. As recorded in section [0018] of Citation 1, the S/P converter 371 is provided with a rate conversion function, realized through a data buffer.

In Citation 2, reference is made to observing the data accumulation amount of the buffer memory data, and to controlling the difference between the 1st clock and the 2nd clock on the basis of the data accumulation amount.

Furthermore, in Figure 25 of Citation 1, changing the data transmission rate at wireless intervals on the basis of the data accumulation amount of the data buffer of the S/P converter 371 is recognized as that which could be easily conceived.

As recorded in Section [0015] of Citation 1, increasing the transmission power in accompaniment with the data rate of the transmission signal becoming high speed, is already known.

That this would be performed within permissible parameters of transmission performance is self evident.

Reference Citation List

1. Japanese Laid Open Patent Publication Hei 11-27059
2. Japanese Laid Open Patent Publication Hei 2-35837